

**Inauguration of VLSI and Embedded System Centre of Excellence by IPESP
at the Department of ECE, SURTECH**

Date and Venue: 14th March 2026, Idea Lab, SurTech

Faculty Coordinator: Dr Anirban Neogi, Dr Arindam Sadhu

No of Participation: 06 faculty members, 04 persons from IPESP, 06 Students volunteers from SurTech

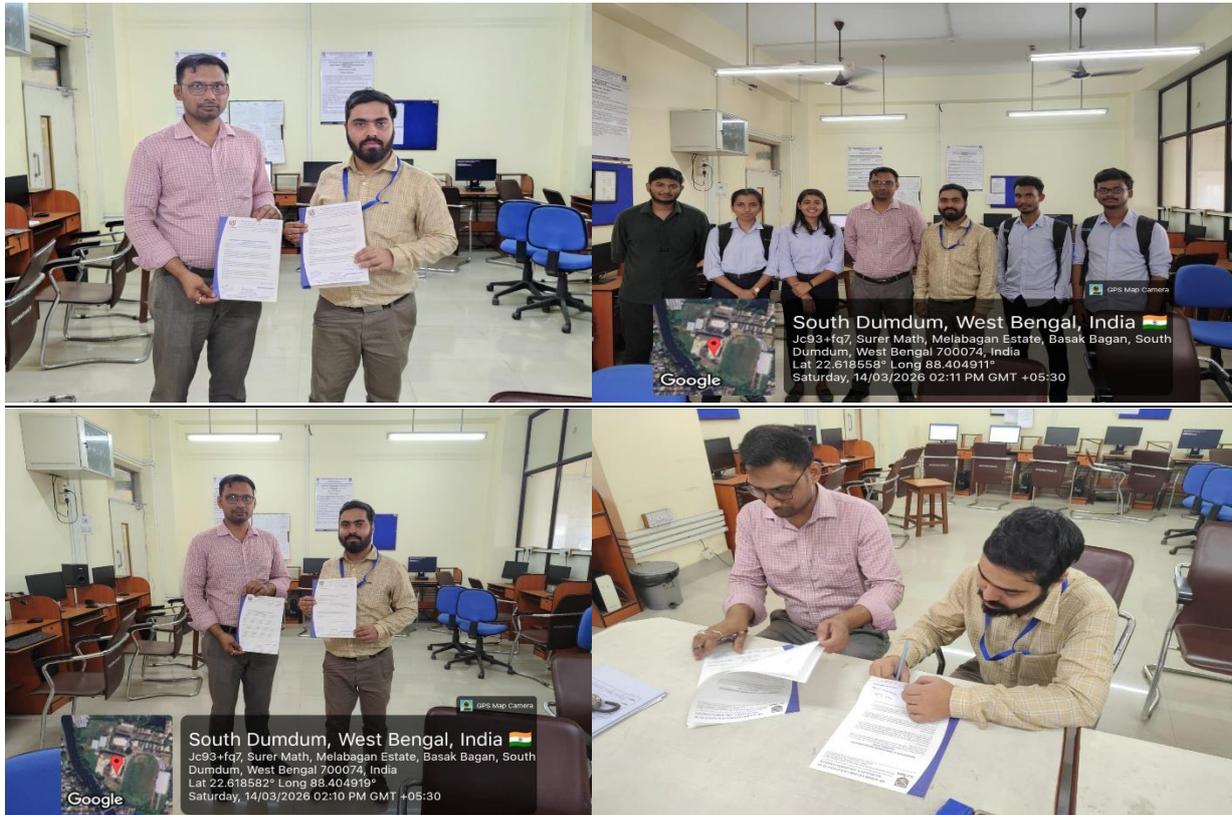
Centre of Excellence Lab by **Innovative Practical Electronics & Software Product Development Solution (IPESP)** was inaugurated on 14th MARCH 2026 under the edges of Department of ECE, Surtech. As per the MOU with the company, the students will be benefitted to prepare projects for national level competitions, to attend industry ready workshops and to avail the opportunity to work in live industry projects through internships.

The founder director of the company Dr Sayan tripathi was present in the occasion. He explained the importance of IPESP COE to the al members. He proposed the concepts of in-house internship by the COE and also mentioned the work culture in live projects by the students.

Outcome of the program

1. Opportunity for the students to work in live projects
2. Possible faculty training as per industry standard
3. Revenue generation for the institute.

Few glimpses of the event,



Dr Anirban Neogi
HOD, ECE Department